

AVR SYSTEM ANALYSIS AND SIMULATION BY USING FOPID AND PARAMETERS VARIATION EFFECTS

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Abstract- The FOPID control units for an AVR system with a fractional filter are a unique fractional order. The main responsibility for controlling the reactive power and voltage level is an automated tensile regulator (AVR). PID controller, sensor, exciter, and stabiliser or amplifier are used for the module system. The system is designed according to state-space technology. The proposed controller must have seven independent parameters. A comparison of the published study with optimal adjusted AVR PID and FOPID controls also shows that the proposed controller is superior. The recommended controls derived from the bode analysis with their frequency response characteristics are shown. Finally, the resilience of the controller design is individually examined for both the parameter uncertainties of AVR system and outside storages introduced into AVR system. Given the overall results presented there are clear improvements to the performance of AVR system by a fractional filter in a proposed FOPID controller, and that the AVR system may successfully be applied to the suggested control.

1 Introduction

Reactive power is the foremost primary aspects for use and design in power systems. A constant tension output is the reactive force balance in the system. By shunting the reactive power injection, by transferring reactive power into the system via tap changes and lowering induction lane reagents through a succession of condensers, one may define the most common reactive power and voltage management systems. AVR systems are commonly used in exciter control systems. Major technique of the reactive power control is AVR generator excitation control. In typical operational conditions, AVR shall maintain the generator terminal voltage constant at varied load levels[1]. The Terminal Voltage Error is used to modify the voltage in AVR loop for an axle control system to administer the terminal-voltage[2]. Four important components – amplifier, sensor, exciter, and generator – are included in a controller exciter. Several articles discussed the AVR system control principles[2-5]. A Real energy losses in electricity generators are a general difficulty to solve. Researchers are studying automated voltage control units (AVR) to reduce these power losses in the power

system. The major goal of this kind of AVR systems is to keep generator voltage at correct level highly precise. This precision enhances the life cycle of power system rated voltage equipment. The AVR system's stability and strength have a considerable impact on the power system's security[1]. Therefore, an efficiency of AVR system continues to be an important topic for the development of new control systems. In particular, many studies have been done to improve the AVR system's transitional reaction to the stability limitations. Researchers have devised several control ways to control the AVR generator voltage so far in an effort to get an enhanced system response. Some of the control techniques presented are the PID fractional order control (PRD) [2-4] [5,6]. Despite these enormous modifications, the scholars focus more on PID controller or its amalgamations because to its straightforward control structure and effectiveness.

PID controller parameters, like other controllers, must be adequately tailored to deliver a good system response. In addition, PID controller settings must be optimally modified in order to reach the best system answer. However, depending on industrial use, this perfectly adjusting method presents a serious problem.

Meta-heuristic optimization algorithms have, in recent years, been favoured in conventional tuning procedures like Ziegler-Nichols and Cohen-Coon, as in most circumstances the results are not optimal[3,10]. Researchers have built AVR System PID controllers that use developmental algorithms like other industrial applications. The FOPID controller is also used in addition to the PID controller to improve generator voltage quality with versatile design options in many technological fields. With two additional parameters than the traditional PID controller, FOPID provides greater design freedom, but five different parameters are more difficult to design for the controller. With regard to the transient responsiveness characters such as over-percentage shots, set-off times, peak times and rising times and steady-state errors, the scientists have aimed to increase their AVR system response. The researchers' goal is in the same direction however, as neither approaches can ensure that the best controller parameter for the AVR system is found, they prefer alternative kinds of algorithms. In other words, using a new algorithm can improve the settings of the controller to improve the system response to AVR. Also, in their above-described optimization techniques researchers have examined many types of goal functions to enhance AVR system response. AVR's [2–5,9,1013,13,18,16–18,20-23] system also includes user-defined target functions, which combine the surplus percentage, setup time, peak time, time increases, stable state error and gain margin. [2] offers an over-hitch percentage, settlement time, time increment, and continuous state mistakes as a typical user-defined target function. This objective function was frequently employed by the scholars to provide fair comparison in between their method and accessible literature.

2 AVR System Model

AVR system, comprising amplifications, exciters, generator and sensor components, [1,2,28], for the output voltage from the synchronised generator stationary to accurate levels. As in Fig.1, AVR system without controller can be built with each component characterised by transfer of first-order function with 'K' gain and the time constant 't' in the AVR system. The K-and μ-parameter value fluctuates within the certain ranges, according to the Table-1, [10-15]. The parameters of the AVR systems components [12-15] shall be specified in study $K_a = 10$, $\dot{a} = 0.1$, $K_e = 1$, $\dot{e} = 0.4$, $K_g = 1$, $\dot{g} = 1.0$, $K_s = 1$, and 's' = 0.01. This phase reaction to these unregulated AVR system parameters is seen in Figure 2. The results suggest that a big static error, a long settlement time and a high overflow have occurred. The AVR system must therefore improve the status and transient features of the region.

AVR System Stabilizer The application of a stabiliser is based mostly on greater relative stability in the

AVR system. For the stabiliser, $U=[vR]$, $Y=[VTI]$, and $X=[VT VF VA VS VM]$, A, B, C and D matrices are indicated: Matrices:

$$A = \begin{bmatrix} -\frac{1}{T_G} & \frac{K_G}{T_G} & 0 & 0 & 0 \\ 0 & -\frac{1}{T_E} & \frac{K_E}{T_E} & 0 & 0 \\ 0 & 0 & -\frac{1}{T_A} & -\frac{K_A}{T_A} & \frac{K_A}{T_A} \\ 0 & \frac{K_S}{T_S T_E} & \frac{K_E K_S}{T_S T_E} & -\frac{1}{T_S} & 0 \\ \frac{K_S}{T_S} & 0 & 0 & 0 & -\frac{1}{T_C} \end{bmatrix} \quad (1)$$

$$B = \begin{bmatrix} 0 & 0 & \frac{K_A}{T_A} & 0 & 0 \end{bmatrix}^T \quad (2)$$

$$C = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 \end{bmatrix}^T \quad (3)$$

The feature of the equation is:

$$\Delta_s(S) = S^5 + C_4 S^4 + C_3 S^3 + C_2 S^2 + C_1 S + C_0 \quad (4)$$

where:

$$C_4 = \frac{1}{T_A} + \frac{1}{T_S} + \frac{1}{T_E} + \frac{1}{T_G} + \frac{1}{T_C} \quad (5)$$

$$C_3 = \left(\frac{1}{T_G} + \frac{1}{T_C}\right)\left(\frac{1}{T_A} + \frac{1}{T_S} + \frac{1}{T_E}\right) + \frac{1}{T_G T_C} + \frac{1}{T_S T_A} + \frac{1}{T_E T_S} + \frac{1}{T_A T_E} + \frac{K_E K_S K_A}{T_A T_S T_E} \quad (6)$$

$$C_2 = \left(\frac{1}{T_G} + \frac{1}{T_C}\right)\left(\frac{1}{T_A} + \frac{1}{T_S} + \frac{1}{T_E}\right) + \frac{1}{T_G T_C} \left(\frac{1}{T_A} + \frac{1}{T_S}\right) + \frac{K_E K_S K_A}{T_A T_S T_E} \left(\frac{1}{T_G} + \frac{1}{T_E}\right) + \frac{1}{T_A T_S T_E} \quad (7)$$

$$C_1 = \frac{1}{T_A T_S T_E} \left(\frac{1}{T_G} + \frac{1}{T_C}\right) + \frac{K_E K_S K_A}{T_A T_S T_E} + \frac{1}{T_G T_C} \left(\frac{1}{T_A} + \frac{1}{T_S} + \frac{1}{T_E}\right) + \frac{K_E K_S K_A}{T_A T_S T_E} \quad (8)$$

$$C_0 = \frac{1 + K_C K_E K_G K_A}{T_C T_G T_A T_S T_E} \quad (9)$$

Transfer function of a Closed loop AVR system with stabiliser:

3 Design of the Proposed FOPID Controller

This section shows design process of a proposed major controller, i.e. FOPID controller AVR system. The model diagram of the controllers and their transfer-functions are given in Figure 3 which also includes traditional PID controllers. Based on model diagrams, conclusion is that there are three separate parameters for a standard PID controller, however the FOPID controller has different parameters. For each controller, the parameters have to be defined.

$$f(\theta) = (1 - e^{\beta})(M_p + E_{SS}) + e^{\beta}(t_s - t_r) \quad (10)$$

While the value of the value of $\beta = 1$ and M_p , t_s , t_r , is the over-shoot, set time, increase time and steady-state error percentages. θ holds the parameters specified for the controller in Table 2.

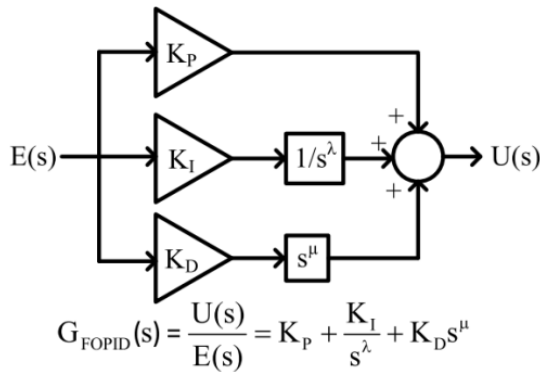


Fig. 1.. Block Diagram of FOPID

PID and FO PID are 10 times since an algorithm begins in a random initial position corresponding to the solutions for the design of the different controllers mentioned above. In general, a termination condition is established to end algorithm for an optimization process. For a variable, this can be a desired number value. When algorithm hits lower value in desired condition final condition can be considered fulfilled. However, such a circumstance is not optimal for optimising the control technology. Because even if the loop takes a very long duration, the algorithm cannot achieve the desired value. Tens of thousands and hundreds. The algorithm can therefore be employed several times (1,5,10,15.....and more) as a further method of system optimization and the optimum system iteration is provided. The powerful metaheuristic algorithms can handle random original position issue in a short search area satisfactorily. For each suggested controller ten separate studies are included in this analysis. In these 10 tests, the value of the cost function is so close that the control parameter value is optimally obtained. Should there not be a substantial difference between these 10 trials. The Table 3 additionally provides optimised controller settings, covering and comparing various PID and FOPID controllers of the research.

4 The Change of the Stabilizer Parameters

The control loop has 3 times TE, TA and TG constant with a rate feedback. A big advantage of the *Rate Feedback System* is that the controller adds

zero to AVR Open loop of the transfer function, which can improve relative stability. The stabiliser loop is a feedback which boosts the circuit's reactivity and stability. In the AVR stabiliser system the continuous response is equated as:

$$V_T(\infty) = \frac{K_E K_G K_A}{1 + K_C K_E K_G K_A} \quad (11)$$

Roots of feature AVR system equation with stabilisers are provided in table V with three alternative values, K_s and T_s . Simulation results for K_s and T_s changes are shown in Figs. 9 and 10. The results show that the variation K_s and T_s impacts the curve maximum, adjustment times, or overflow, but that it does not affect the final terminal voltage values of the stable and unstable systems. A higher K_s value and a lower T_s result in quick exceedance and settlement time. Increasing the time of the stabiliser decreases the time of both increase and peak. In contrast, increasing stabiliser loop gain increases both higher and higher times and reduces higher gain percentages.

5 Fractional Order PID Controller

A Calculus Fractional FOPID controller provides non integer command differentiation and integration. These non-triggers give greater flexibility for design in different fields of engineering including theory of control, signal processing, robotics, etc. This attracted the attention of scholars, especially in the control system sector, to the fractional computation. Riemann–Livouville, Grunwald-Letnikov and Caputo are famous and widely used for their definitions. In addition, approximation approaches are frequently favoured because it is difficult to calculate the definitions presented. One of these approximative approaches is the Oustaloup approximation method for converging fractional order transfer functions.

This work uses the Oustaloup procedure for the approximation of the transfer function to produce fractional transmission functions in the frequency field ω directories $[10^{-5} \text{ } 10^5] \text{ r/s}$.

In the form of a PID_μ is written a FOPID controller containing the fractional integrator, with ϵ and μ as the integrator's and the differentiator's commands. (1) represents a general FOPID controller transfer function with K_P , K_I and K_D proportional, integral and derivative gains, respectively, whose block diagram is illustrated in Fig. 3d.

$$G_{FOPID}(s) = K_p + \frac{K_i}{s^\lambda} + K_d s^\mu \quad (12)$$

where β and μ are, respectively, the order of the integrator and differentiator. The *FOPID controller* contains two additional settings, i.e. μ and β , to be tuned, different from the typical PID controller. While, these external parameters enable more control system design possibilities, they improve

complexity of controller and make tweaking of parameters highly complex than normal PID controllers.

Fig. 2.. Simulation of FOPID

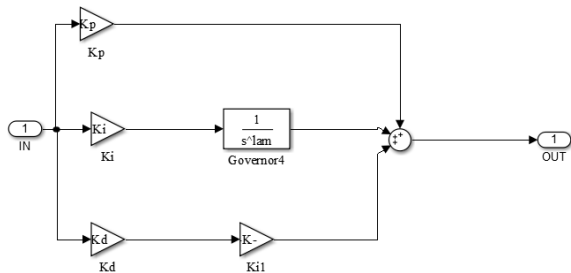


Table 1 AVR system Transfer functions components

	Transfer function	Limits of parameters		Used parameters
		Gain	Time constant (s)	
Amplifier	$T(s) = K_a / (1 + \tau_a s)$	$10 \leq K_a \leq 40$	$0.02 \leq \tau_a \leq 0.1$	$K_a = 10, \tau_a = 0.1$
Exciter	$T(s) = \frac{K_e}{1 + \tau_e s}$	$1 \leq K_e \leq 10$	$0.4 \leq \tau_e \leq 1.0$	$K_e = 1, \tau_e = 0.4$
Generator	$T(s) = \frac{K_g}{1 + \tau_g s}$	$0.7 \leq K_g \leq 1.0$	$1.0 \leq \tau_g \leq 2.0$	$K_g = 1, \tau_g = 1.0$
Sensor	$T(s) = \frac{K_s}{1 + \tau_s s}$	$0.9 \leq K_s \leq 1.1$	$0.001 \leq \tau_s \leq 0.06$	$K_s = 1, \tau_s = 0.01$

6 Results

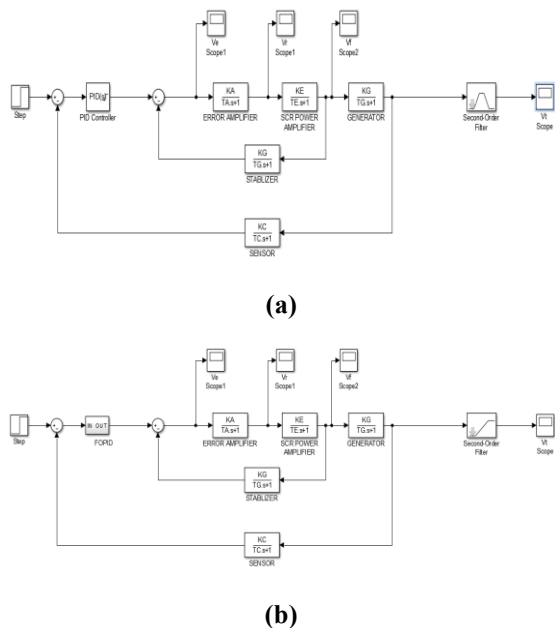


Fig3. Transfer function simulations of the controllers. a: PID, b:FOPID

TG=4;
 KG=0.7;
 KA=5;
 TA=0.02;
 KE=10;
 TE=0.5;
 KS=0.02;
 TS=0.013;
 TC=0.001;

KC=0.1;
 Kp=0.1;
 Ki=0.02;
 Kd=0.05;
 s=1;
 lam=3;
 u=0.1;

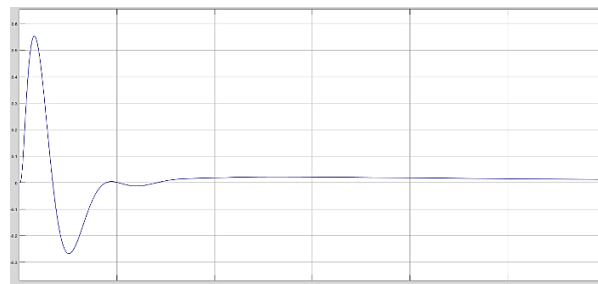


Fig. 4. AVR system terminal voltage step response with PID , stabilizer (solid) and without compensation (dash) with 0.5 amplitude with time period 1 sec and from 2 secs amplitude doesn't settle down to zero.

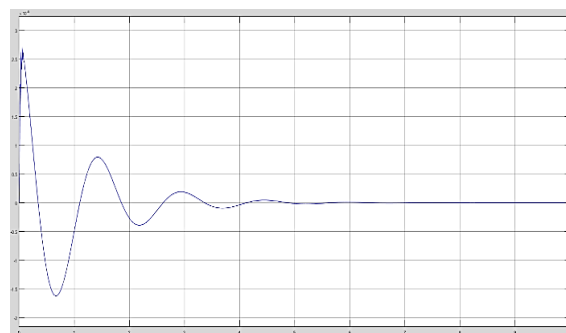


Fig. 5 AVR system terminal voltage step response with FOPID, stabilizer (solid) and without compensation (dash) with amplitude of 0.2×10^{-5} in 5 secs.

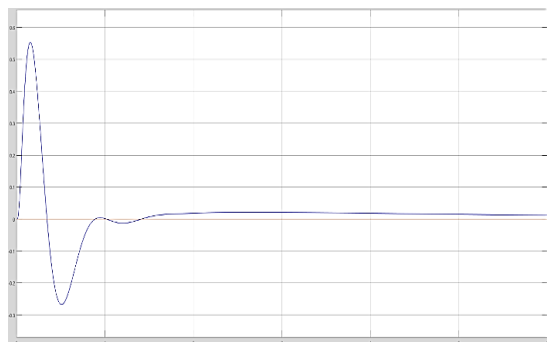


Fig. 6 AVR system terminal voltage step response with PID and FOPID, stabilizer (solid) and without compensation (dash)

7 Conclusion

The objective of the present research is to develop regulatory performance of the normally used AVR system with the single fractional filter FOPID controller. This study also seeks to prove the efficiency of proposed FOPID controller and to make the suggested control of fractional filter a beneficial impact. For the identical AVR system the PID and FOPID controllers, which perfectly tuned their literature settings, are applied. Time domain analysis are performed for various controllers and superiority of suggested controller is exposed. Box diagrams analyse AVR system stability with different controllers. In addition, the durability of their designed controllers is separately examined by both the intravenous parameters of AVR system and external disorder injecting into AVR system. Consequently, results of research analyses that their fractional filter by proposed FOPID controller improved substantively regulatory performance of AVR system. Finally, AVR system was simulated and several parameter effects are examined.

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